

## DATA OUTPUT CIRCUIT FOR REDUCING SKEW OF DATA SIGNAL

### Abstract of the Disclosure

In a data output circuit for reducing a skewing error of a data signal, a first  
5 inversion unit receives a first data signal of an operating voltage level and inverts the  
received first data signal to obtain a first inverted data signal. If a first power supply  
voltage of an output voltage level is different from a second power supply voltage with  
the operating voltage level by at least a predetermined voltage level, a first voltage  
compensation unit compensates for the voltage level of the first inverted data signal to  
10 obtain a first driving signal. A second inversion unit receives a second data signal with  
the operating voltage level and inverts the received second data signal to obtain a  
second inverted data signal. If the levels of the first and second power supply voltages  
are different by at least a predetermined voltage level, a second voltage compensation  
unit compensates for the voltage level of the second inverted data signal to obtain a  
15 second driving signal. A driver unit receives the first and second driving signals and  
outputs an output data signal with a logic level that is opposite to the logic levels of the  
first and second driving signals.

J:\sam\0424\424patapp2.doc